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Complete if Known

Application Number	10/671,233
Filing Date	09/25/03
First Named Inventor	Jian H. Zhao
Art Unit	
Examiner Name	Tan N. Tran
Attorney Docket Number	N/A

Sheet	1	of	1
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U. S. PATENT DOCUMENTS

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Sheet 1	of 1	Attorney Docket Number	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		K. ASANO, et. al, 5kV 4H-SiC SEJFET with Low RonS of 69 m-ohm-cm2, Proc. of 14th Intl. Symp. on Power Semiconductor Devices & IC 2002, June 3, 2002,	
		pp. 61-64, IEEE, Piscataway, NJ, USA.	
		J. Nishizawa, et. al., The 2.45 GHz 36 W CW Si recessed gate type SIT with high gain and high voltage operation, IEEE Trans. on Electron Devices,	
		Feb. 2000, pp.482-487, Vol. 4, No. 2, IEEE, Piscataway, NJ, USA.	
		H. Onose, et. al., 2kV 4H-SiC junction FETs, Materials Science Forum, 2002, pp. 1227-1230, Vols. 389-393, Trans Tech Publications, Switzerland.	
		Jian H. Zhao et. al, 3.6 m-ohm-cm2, 1,726 4H-SiC normally-off trench-and-implanted vertical JFETs, 2003, pp.50-52, ISPSD, IEEE, Piscataway, NJ, USA	
		Jian H. Zhao, et al., Demonstration of a high performance 4H-SiC vertical junction field effect transistor without epitaxial regrowth, Feb. 2003, pp. 321-323,	
		Electronics Letters, IEE, Stevenage, SG1 2SD, UK.	

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